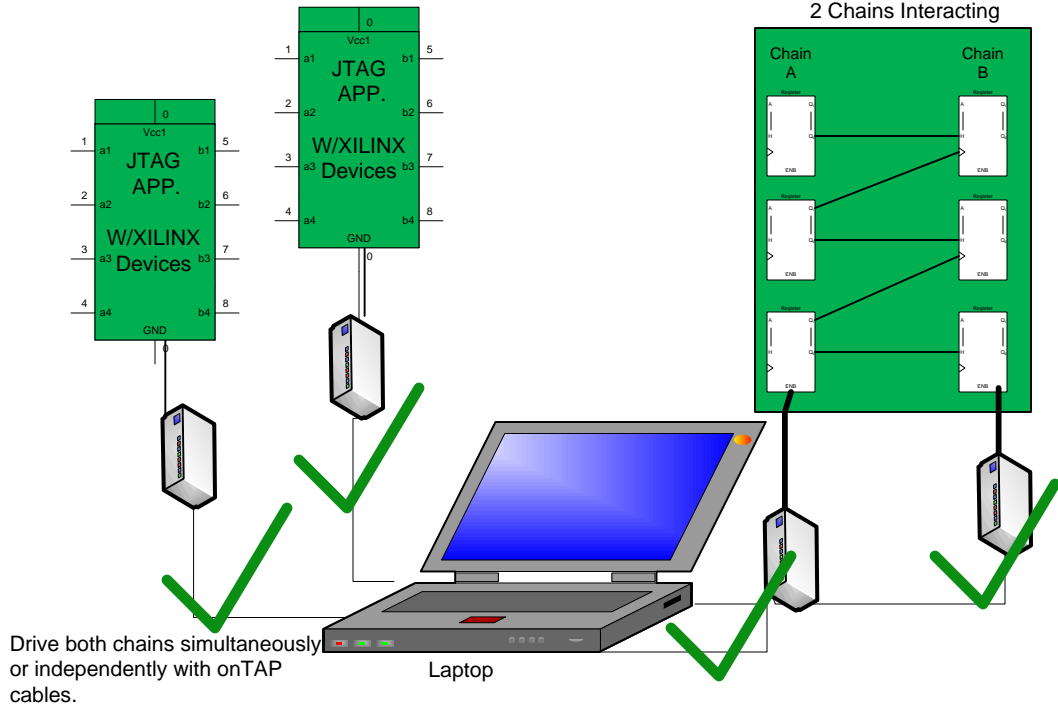


1

# JTAG Apps Using onTAP Cables

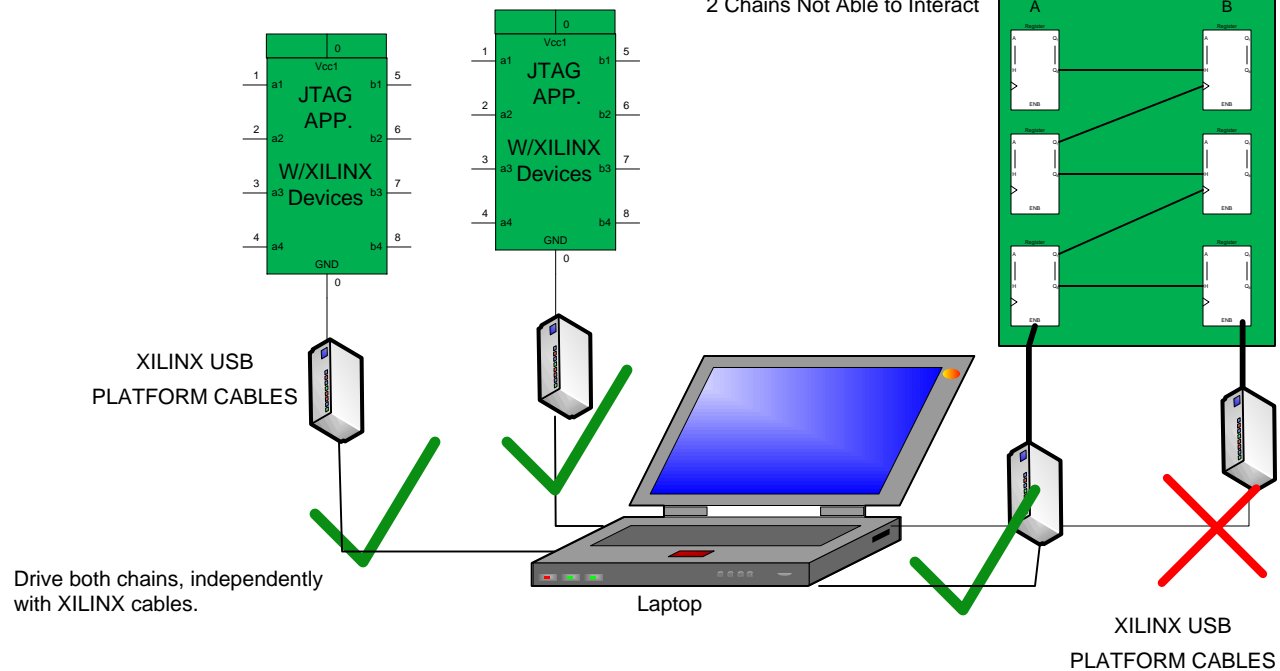


The diagram #1 shows a typical and likely boundary scan/JTAG test/debug/programming operation using two or more onTAP USB JTAG controllers to access and run independent JTAG applications from one PC. On the left of the laptop, there are two separate applications that may or may not run in parallel. However, on the right, there is an example of an application that has two chains that need to interact, and require simultaneous JTAG test connections.

In diagram #2, the same scenario played out using Xilinx Platform USB Cables prohibits the functionality of the applications on the right hand side. The importance of running two chains simultaneously is far reaching for our customers. From a JTAG test and programming perspective, many of our customers are using Xilinx USB Platform Cables to test and program individual Xilinx devices in their application(s). Often times, JTAG applications include more than one JTAG chain, and it is common to have the chains interacting. What is also normal, is an engineer will manipulate the Xilinx device with Xilinx tools, then have to unplug the Xilinx USB cable, shut down ChipScope and/or Impact, plug in our cable, then start our software to run JTAG tests and program or configure flash, memory, or other logic, then may have to switch back to the Xilinx setup to re-evaluate changes to Xilinx devices, or to prepare for the next board(s).

Offering capability to utilize just one JTAG test and programming cable increases productivity, saves money, and improves work flow.

# 2 JTAG Apps Using Xilinx USB Cables



JTAG APP.  
W/XILINX Devices  
2 Chains Not Able to Interact

XILINX USB PLATFORM CABLES